

## CAREER OBJECTIVE

To be involved in work where I can utilize skill and creativity involved with system that effectively contributes to the growth of organization.

## EDUCATION

- 2015 – 2019 Bachelor of Technology (ECE)**  
PYDAH COLLEGE OF ENGINEERING AND TECHNOLOGY  
70.0 %
- 2013 – 2015 Higher Secondary Education**  
NRI Junior College  
88.8%
- 2012 – 2013 Secondary Education**  
MAHARSHI VIDYA NIKETAN  
9.0 CGPA

## PROJECTS

- DESIGN AND IMPLEMENTATION OF 32 BIT ADDER USING KOGGESTONE AND SPAINING TREE**  
Adders play a vital role in digital signal Processing systems. Parallel prefix adders have better delay performance in **VLSI** designs.
- LIBRARY MANAGEMENT SYSTEMS**  
Development of a front end web page Library management system using HTML, CSS.

## INTERNSHIP

- May 2018 TELECOM NETWORK**  
BSNL (Bharat Sanchar Nigam Limited)  
Trained on mobile, broadband, switching & transmission technologies

## WORKSHOPS ATTENDED

- Workshop on **PCB** design at Pydah College of Engg.& technology
- Cisco Networking** at JNTU vizianagaram

## PERSONALITY TRAITS

- Adaptability
- Team Player
- Leadership Qualities
- Planning
- Analyzer
- Focused

## TECHNICAL SKILLS

- Fundamentals of C, C++
- HTML
- XILINX
- MS Excel
- MS Word

## HOBBIES

- Reading books
- Travelling
- Cooking
- Listening music

## PERSONAL INFORMATION

Date of Birth : 23-07-1998  
Mother's Name : Sahu Sabitha  
Father's Name : Sahu Srinivasa  
Languages Known : English, Telugu, Hindi, Oriya

## EXPERIENCE

- Process Executive -1 in PATRA India For 1 year